

```

RRRRRRRRRRRRRRR      MMM      MMM      SSSSSSSSSSSSSS
RRRRRRRRRRRRRRR      MMM      MMM      SSSSSSSSSSSSSS
RRRRRRRRRRRRRRR      MMM      MMM      SSSSSSSSSSSSSS
RRR      RRR      MMMMMM      MMMMMM      SSS
RRR      RRR      MMMMMM      MMMMMM      SSS
RRR      RRR      MMMMMM      MMMMMM      SSS
RRR      RRR      MMM      MMM      MMM      SSS
RRR      RRR      MMM      MMM      MMM      SSS
RRR      RRR      MMM      MMM      MMM      SSS
RRRRRRRRRRRRRRR      MMM      MMM      SSSSSSSSSS
RRRRRRRRRRRRRRR      MMM      MMM      SSSSSSSSSS
RRRRRRRRRRRRRRR      MMM      MMM      SSSSSSSSSS
RRR      RRR      MMM      MMM      MMM      SSS
RRR      RRR      MMM      MMM      MMM      SSS
RRR      RRR      MMM      MMM      MMM      SSS
RRR      RRR      MMM      MMM      MMM      SSS
RRR      RRR      MMM      MMM      MMM      SSS
RRR      RRR      MMM      MMM      SSSSSSSSSSSSSS
RRR      RRR      MMM      MMM      SSSSSSSSSSSSSS
RRR      RRR      MMM      MMM      SSSSSSSSSSSSSS

```

53

Syn

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11. *Journal of the American Medical Association*, 277, 1996, 1025-1026.

1

```
RRRRRRRR  MM      MM  SSSSSSSS  000000  SSSSSSSS  DDDDDDDD  FFFFFFFF  PPPPPPPP
RRRRRRRR  MM      MM  SSSSSSSS  000000  SSSSSSSS  DDDDDDDD  FFFFFFFF  PPPPPPPP
RR      RR  MMMM  MMMM  SS      00      00  SS      DD      DD  FF      PP      PP
RR      RR  MMMM  MMMM  SS      00      00  SS      DD      DD  FF      PP      PP
RR      RR  MM  MM  MM  SS      00      0000  SS      DD      DD  FF      PP      PP
RRRRRRRR  MM      MM  SSSSSS  00  00  00  SSSSSS  DD      DD  FFFFFFFF  PPPPPPPP
RRRRRRRR  MM      MM  SSSSSS  00  00  00  SSSSSS  DD      DD  FFFFFFFF  PPPPPPPP
RR      RR  MM      MM      SS  0000  00  SS      DD      DD  FF      PP
RR      RR  MM      MM      SS  0000  00  SS      DD      DD  FF      PP
RR      RR  MM      MM      SS  00      00  SS      DD      DD  FF      PP
RR      RR  MM      MM      SS  00      00  SS      DD      DD  FF      PP
RR      RR  MM      MM  SSSSSSSS  000000  SSSSSSSS  DDDDDDDD  FF      PP
RR      RR  MM      MM  SSSSSSSS  000000  SSSSSSSS  DDDDDDDD  FF      PP
```

```
LL      IIIIII  SSSSSSSS
LL      IIIIII  SSSSSSSS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SSSSSS
LL      II      SSSSSS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SS
LLLLLLLLLL  IIIIII  SSSSSSSS
LLLLLLLLLL  IIIIII  SSSSSSSS
```


(2)	61
(3)	84

DECLARATIONS
RMS\$SETDFPROT - SET DEFAULT FILE PROTECTION ROUTINE

B
C
D
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```
0000 1 $BEGIN RMSOSDFP,000,RMSRMS,<SET DEFAULT FILE PROTECTION>
0000 2
0000 3
0000 4 *****
0000 5 *
0000 6 * COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
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0000 20 *
0000 21 * DIGITAL ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS
0000 22 * SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.
0000 23 *
0000 24 *
0000 25 *****
0000 26
0000 27 ++
0000 28 Facility: rms32
0000 29
0000 30 Abstract:
0000 31 this routine sets the default file protection in
0000 32 the process i/o control page.
0000 33
0000 34 Environment:
0000 35 star processor running starlet exec.
0000 36
0000 37 Author: l f laverdure, creation date: 22-APR-1977
0000 38
0000 39 Modified By:
0000 40
0000 41 V03-003 RAS0237 Ron Schaefer 16-Jan-1984
0000 42 Change LMP0133 to use CTL$GL_PCB, instead of SCH$GL_CURPCB
0000 43 so this code works on the secondary of a dual processor.
0000 44
0000 45 V03-002 LMP0133 L. Mark Pilant, 3-Aug-1983 15:05
0000 46 Get default protection from PCB instead of P1 space.
0000 47
0000 48 V03-001 KBT0192 Keith B. Thompson 23-Aug-1982
0000 49 Reorganize psects and rename entry point to single '$'
0000 50
0000 51 V02-006 REFORMAT P S Knibbe 25-Jul-1980
0000 52
0000 53 V005 RAN0002 R A Newell 18-SEP-1978 09:19
0000 54 rms32 isam modification. redefinition of entry point to
0000 55 resolve out of range branches.
0000 56
0000 57 01 -
```


RMSOSDFP
V04-000

SET DEFAULT FILE PROTECTION

C 16

16-SEP-1984 01:30:52 VAX/VMS Macro V04-00
5-SEP-1984 16:25:29 [RMS.SRC]RMSOSDFP.MAR;1

Page 2
(1)

0000 58 :--
0000 59


```
0000 61      .SBTTL  DECLARATIONS
0000 62
0000 63  :
0000 64  : Include Files:
0000 65  :
0000 66  :
0000 67  :
0000 68  : Macros:
0000 69  :
0000 70
0000 71      $PSLDEF
0000 72      $RMSDEF
0000 73      $PCBDEF
0000 74
0000 75  :
0000 76  : Equated Symbols:
0000 77  :
0000 78  :
0000 79  :
0000 80  : Own Storage:
0000 81  :
0000 82
```



```
0000 84      .SBTTL RMS$SETDFPROT - SET DEFAULT FILE PROTECTION ROUTINE
0000 85
0000 86 :++
0000 87 : RMS$SETDFPROT - Set default file protection
0000 88 :
0000 89 :   this routine optionally returns and/or sets the value of the
0000 90 :   default file protection word in the process i/o control page.
0000 91 :
0000 92 :   Calling sequence:
0000 93 :
0000 94 :       entered from exec as a result of user's calling sys$setdfprot
0000 95 :
0000 96 :   Input Parameters:
0000 97 :
0000 98 :       ap      user's argument list addr
0000 99 :               first argument gives the address of the new default file
0000 100 :               protection word if any, else 0.
0000 101 :               contains a second argument optionally giving the address of
0000 102 :               a word in which the current default protection may be returned.
0000 103 :
0000 104 :   Implicit Inputs:
0000 105 :
0000 106 :
0000 107 :   Output Parameters:
0000 108 :
0000 109 :       r1      destroyed
0000 110 :       r0      status code
0000 111 :       (note: this routine uses r0-r3 as saved in the system vector entry mask)
0000 112 :
0000 113 :   Implicit Outputs:
0000 114 :
0000 115 :       optionally, pio$gw_dfprot set to the value of the input word
0000 116 :       and/or the current default protection is returned to the caller
0000 117 :
0000 118 :   Completion Codes:
0000 119 :
0000 120 :       standard rms, in particular, suc and ial.
0000 121 :
0000 122 :   Side Effects:
0000 123 :
0000 124 :       none
0000 125 :
0000 126 :   --
0000 127
```



```
0000 129 $ENTRY RMS$SETDFPROT
0000 130 $TSTPT SETDFPROT
0006 131
0006 132 ;
0006 133 ; see if the caller wants the protection returned
0006 134 ;
0006 135
52 04 AC D0 000B 136 RMSERR IAL ; anticipate error
09 13 000F 137 MOVL 4(AP),R2 ; get 1st argument
53 62 B0 0017 138 BEQL 3$ ; branch if no value to set
51 08 AC D0 001A 139 IFNORD #2,(R2),EXIT ; is the word readable?
12 13 001E 140 MOVW (R2),R3 ; save new value
50 00000000'GF D0 0020 141 3$: MOVL 8(AP),R1 ; possible address of return length
61 0114 C0 B0 0026 142 BEQL 5$ ; if caller doesn't want it, o.k.
52 D5 0032 143 IFNOWRT #2,(R1),EXIT ; writeable?
OF 13 0034 144 MOVL G^CTL$GL_PCB,R0 ; get current process PCB address
0036 145 MOVW PCB$L_DEFPROT(R0),(R1) ; yes, so return it to the caller
0045 146 5$: TSTL R2 ; was there a new value?
0048 147 BEQL 7$ ; branch if not
0048 148 $CMKRNL_S SETPROT ; else copy it over
0048 149 7$: RMSSUC
0048 150 EXIT:
04 004C 151 SSB #16,R0
004D 152 RET
004D 153
004D 154 SETPROT:
50 00000000'GF 0000 004D 155 .WORD 0
0114 C0 53 D0 004F 156 MOVL G^CTL$GL_PCB,R0 ; get current process PCB address
B0 0056 157 MOVW R3,PCB$L_DEFPROT(R0) ; else copy it over
04 005B 158 RET
005C 159
005C 160 .END
```


RMSOSDFP
Symbol table

SET DEFAULT FILE PROTECTION

G 16

16-SEP-1984 01:30:52 VAX/VMS Macro V04-00
5-SEP-1984 16:25:29 [RMS.SRC]RMSOSDFP.MAR;1

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(4)

```
$$PSECT_EP      = 00000000
$$RMSTEST       = 0000001A
$$RMS_PBUGCHK   = 00000010
$$RMS_TBUGCHK   = 00000008
$$RMS_UMODE     = 00000004
CTL$GC_PCB      ***** X 01
EXIT            00000048 R 01
PCBSL_DEFPROT   = 00000114
PIOSA_TRACE     ***** X 01
RMS$SETDFPROT   = FFFFFFFE RG 01
RMS$IAL         = 0001854C
SETPROT         0000004D R 01
SYSCMKRNL       ***** GX 01
TPTSL_SETDFPROT ***** X 01
```

+-----+
! Psect synopsis !
+-----+

PSECT name	Allocation	PSECT No.	Attributes
. ABS .	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
RMSRMS	0000005C (92.)	01 (1.)	PIC USR CON REL GBL NOSHR EXE RD NOWRT NOVEC BYTE
\$ABS\$	00000000 (0.)	02 (2.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE

+-----+
! Performance indicators !
+-----+

Phase	Page faults	CPU Time	Elapsed Time
Initialization	36	00:00:00.09	00:00:00.48
Command processing	142	00:00:00.68	00:00:05.95
Pass 1	204	00:00:04.36	00:00:16.35
Symbol table sort	0	00:00:00.41	00:00:00.57
Pass 2	44	00:00:00.84	00:00:02.96
Symbol table output	3	00:00:00.03	00:00:00.16
Psect synopsis output	1	00:00:00.02	00:00:00.17
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	432	00:00:06.45	00:00:26.70

The working set limit was 1350 pages.
21484 bytes (42 pages) of virtual memory were used to buffer the intermediate code.
There were 20 pages of symbol table space allocated to hold 403 non-local and 4 local symbols.
160 source lines were read in Pass 1, producing 13 object records in Pass 2.
22 pages of virtual memory were used to define 21 macros.

+-----+
! Macro library statistics !
+-----+

Macro library name	Macros defined
-----	-----
_\$255\$DUA28:[RMS.OBJ]RMS.MLB;1	8
_\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	3
_\$255\$DUA28:[SYSLIB]STARLET.MLB;2	6
TOTALS (all libraries)	17

539 GETS were required to define 17 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:RMSOSDFP/OBJ=OBJ\$:RMSOSDFP MSRC\$:RMSOSDFP/UPDATE=(ENH\$:RMSOSDFP)+EXECML\$/LIB+LIB\$:RMS/LIB

0330 AH-BT13A-SE
VAX/VMS V4.0

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